

SARVEPALLI RADHAKRISHNAN UNIVERSITY BHOPAL (M.P.)

RKDF INSTITUTE OF SCIENCE & TECHNOLOGY

TIME TABLE FOR EVEN SEM EXAMINATION (SUPPLEMENTARY) - DEC 2023

BACHELOR OF ENGINEERING - VI SEMESTER (EX) (BATCH 2019-2020 ADMITTED ONWARDS)

Branch / DATE	05-12-2023	06-12-2023	07-12-2023	08-12-2023	09-12-2023
EE	EE-61 Power System Protection	EE-62 Control System (CS)	EE-63 Power System-II (PS-II)	EE-64(A) Signals & Systems	EE-65(A) Digital Signal Processing (DSP)
EEE	EEE-61 Signals & Systems	EEE-62 Control System (CS)	EEE-63 Power System-II (PS-II)	EEE-64(B) Power Plant Engineering	EEE-65(A) Digital Signal Processing (DSP)
CE	CE61 TRANSPORTATION ENGINEERING II	CE62 RCC DESIGN	CE63 STEEL DESIGN	CE64 (A) FOUNDATION ENGINEERING	CE65 (C) METRO SYSTEMS AND ENGINEERING
EC	EC61 CONTROL SYSTEMS	EC62 VLSI CIRCUITS & SYSTEMS	EC63 ANTENNA AND WAVE PROPAGATION	EC64(A) PROGRAM ELECTIVE -2- (EMBEDDED SYSTEM)	EC65(A) OPEN ELECTIVE-2 (COMPUTER NETWORK)
EI	EI61(B) DEPARTMENTAL ELECTIVE-II INTELLIGENT INSTRUMENTATION	EI62 VLSI CIRCUITS & SYSTEMS	EI63 POWER ELECTRONICS	EI64 EMBEDDED SYSTEM	EI65(A) OPEN ELECTIVE-II SCADA SYSTEM
CS	CS 61 CYBER LAW AND ETHICS	CS-62 ADVANCE COMPUTER ARCHITECTURE	CS-63 SOFTWARE ENGINEERING & PROJECT MANAGEMENT	CS 64 COMPILER DESIGN	CS 65 COMPUTER NETWORKING
IT	IT-61 CYBER LAW AND ETHICS	IT-62 EMBEDDED SYSTEM	IT-63 SOFTWARE ENGINEERING & PROJECT MANAGEMENT	IT 64 WIRELESS AND MOBILE COMPUTING	IT 65 COMPILER DESIGN
ME	ME 61 (A) AUTOMOBILE	ME 62 (B) (ENERGY CONSERVATION AND MANAGEMENT	ME 63 MANUFACTURING PROCESS -II	ME 64 MACHINE DESIGN-I	ME 65 I.C. ENGINE
ET	ET 61 CONTROL SYSTEMS	ET 62 VLSI CIRCUITS & SYSTEMS	ET 63 ANTENNA AND WAVE PROPAGATION	ET64(A) EMBEDDED SYSTEM	ET65(A) NANOTECHNOLOGY


Exam Controller
SRK University, Bhopal