



# SARVEPALLI RADHAKRISHNAN UNIVERSITY, BHOPAL

Scheme of examination as per AICTE model Curriculum w.e.f July 2020

## Grading System

Course Name –Master of Technology (VLSI)

Semester I / Year : I

Scheme for 2020 Admitted Students onwards

S. No.	Subject Code	Category	Subject Name	Maximum Marks Allotted					Hours/ Week			Credit	Total Marks
				Theory			Practical		L	T	P		
				End Sem.	Mid Sem	Quiz, Assignment	End Sem	Term Work					
1	MTVD 11	CORE	RTL SIMULATION AND SYNTHESIS WITH PLDs	100	30	30	50	50	3	1	4	6	260
2	MTVD 12	CORE	MICRO CONTROLLER AND PROGRAMMABLE DIGITAL SIGNAL PROCESSORS	100	30	30	50	50	3	1	4	6	260
3	MTVD 13	CORE	RESEARCH METHODOLOGY AND IPR	100	30	30	-	-	3	1	-	4	160
4	MTVD 14	PE	ELECTIVE-I	100	30	30	-	-	3	1	-	4	160
5	MTVD 15	PE	ELECTIVE-II	100	30	30	-	-	3	1	-	4	160
6	MTVD 16	AUDIT	AUDIT - I	-	-	-	-	-	2	0	0	0	-
<b>TOTAL</b>				<b>500</b>	<b>150</b>	<b>150</b>	<b>100</b>	<b>100</b>	<b>17</b>	<b>5</b>	<b>8</b>	<b>24</b>	<b>1000</b>

L: Lecture

T:Tutorial

P:Practical

ELECTIVE -I	ELECTIVE -II	AUDIT COURSE- I
MTVD14(A).DIGITAL SIGNAL AND IMAGE PROCESSING	MTVD15(A). PARALLEL PROCESSING	1.ENGLISH FOR RESEARCH PAPER WRITING
MTVD14(B). PROGRAMMING LANGUAGES FOR EMBEDDED SOFTWARE	MTVD15(B).SYSTEM DESIGN WITH EMBEDDED LINUX	2.DISASTER MANAGEMENT
MTVD14(C).VLSI SIGNAL PROCESSING	MTVD15(C). VLSI DESIGN	3.SANSKRIT FOR TECHNICAL KNOWLEDGE
		4.VALUE EDUCATION



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## Grading System

**Course Name –Master of Technology (VLSI)**

**Semester II / Year : I**

**Scheme for 2020 Admitted Students onwards**

S. No.	Subject Code	Category	Subject Name	Maximum Marks Allotted					Hours/ Week			Credit	Total Marks
				Theory			Practical		L	T	P		
				End Sem.	Mid Sem	Quiz, Assignment	End Sem	Term Work					
1	MTVD 21	CORE	ANALOG AND DIGITAL CMOS VLSI DESIGN	100	30	30	50	50	4	1	4	6	260
2	MTVD 22	CORE	VLSI DESIGN VERIFICATION AND TESTING	100	30	30	50	50	4	1	4	6	260
4	MTVD 23	PE	ELECTIVE III	100	30	30	-	-	4	1	-	5	160
5	MTVD 24	PE	ELECTIVE IV	100	30	30	-	-	4	1	-	5	160
	MTVD 25	CORE	MINI PROJECT WITH SEMINAR	-	-	-	100	60	-	-	4	2	160
6	MTVD 26	AUDIT	AUDIT - II	-	-	-	-	-	2	-	-	-	-
<b>TOTAL</b>				<b>400</b>	<b>120</b>	<b>120</b>	<b>200</b>	<b>160</b>	<b>18</b>	<b>4</b>	<b>8</b>	<b>24</b>	<b>1000</b>

**L: Lecture**

**T:Tutorial**

**P:Practical**

ELECTIVE -III	ELECTIVE -IV	AUDIT COURSE - II
MTVD23(A) .MEMORY TECHNOLOGIES	MTVD24(A). COMMUNICATION BUSES AND INTERFACES	1. CONSTITUTION OF INDIA
MTVD23(B).SoC DESIGN	MTVD24(B).VLSI TECHNOLOGY	2. PEDAGOGY STUDIES
MTVD23(C) .LOW POWER VLSI DESIGN	MTVD24(C)PHYSICAL DESIGN AUTOMATION	3. STRESS MANAGEMENT BY YOGA
		4. PERSONALITY DEVELOPMENT THROUGH LIFE ENLIGHTENMENT SKILLS



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## Grading System

Course Name –Master of Technology (VLSI)

Semester III / Year : II

Scheme for 2020 Admitted Students onwards

S. No.	Subject Code	Category	Subject Name	Maximum Marks Allotted					Hours/ Week			Credit	Total Marks
				Theory			Practical		L	T	P		
				End Sem.	Mid Sem	Quiz, Assignment	End Sem	Term Work					
	MTVD31	PE	ELECTIVE V	100	30	30	-	-	4	1	-	5	160
	MTVD32	OE	OE	100	30	30	-	-	4	1	-	5	160
1	MTVD DP(1)	DESSERTATION	DESSERTATION (PHASE-I)	-	-	-	400	280	-	-	20	10	680
<b>TOTAL</b>				<b>200</b>	<b>60</b>	<b>60</b>	<b>400</b>	<b>280</b>	<b>8</b>	<b>2</b>	<b>20</b>	<b>20</b>	<b>1000</b>

**L: Lecture**

**T:Tutorial**

**P:Practical**

<b>ELECTIVE -V</b>	<b>OPEN ELECTIVE (OE)</b>
MTVD31(A).CAD OF DIGITAL SYSTEM	MTVD32(A).BUSINESS ANALYTICS
MTVD31(B).SELECTED TOPICS IN MATHEMATICAL	MTVD32(B).OPERATIONS RESEARCH
MTVD31(C).NANO MATERIAL AND TECHNOLOGY	MTVD32(C) COST MANAGEMENT OF ENGINEERING PROJECTS



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## Grading System

Course Name –Master of Technology (VLSI)

Semester IV / Year : II

Scheme for 2020 Admitted Students onwards

S. No.	Subject Code	Category	Subject Name	Maximum Marks Allotted					Hours/ Week			Credit	Total Marks
				Theory			Practical		L	T	P		
				End Sem.	Mid Sem	Quiz, Assignment	End Sem	Term Work					
1	MTVD DP (II)	DESSERTATION	DESSERTATION (PHASE-II)	-	-	-	500	500	-	-	30	15	1000
<b>TOTAL</b>				-	-	-	<b>500</b>	<b>500</b>	-	-	<b>30</b>	<b>15</b>	<b>1000</b>

**L: Lecture**

**T:Tutorial**

**P:Practical**