



SARVEPALLI RADHAKRISHNAN UNIVERSITY, **Bhopal (M.P.)**

B.E. 301 - ENGINEERING MATHEMATICS-III

Unit I

Functions of complex variables : Analytic functions, Harmonic Conjugate, Cauchy- Riemann Equations, Line Integral, Cauchy's Theorem, Cauchy's Integral Formula, Singular Points, Poles & Residues, Residue Theorem , Application of Residues theorem for evaluation of real integrals

Unit II

Errors & Approximations, Solution of Algebraic & Trancedental Equations (Regula Falsi , Newton-Raphson, Iterative, Secant Method), Solution of simultaneous linear equatins by Gauss Elimination, Gauss Jordan, Crout's methods , Jacobi's and Gauss-Siedel Iterative methods

Unit III

Difference Operators, Interpolation (Newton Forward & Backward Formulae, Central Interpolation Formulae, Lagrange's and divided difference formulae), Numerical Differentiation and Numerical Integration.

Unit IV

Solution of Ordinary Differential Equations(Taylor's Series, Picard's Method, Modified Euler's Method, Runge-Kutta Method, Milne's Predictor & Corrector method), Correlation and Regression, Curve Fitting (Method of Least Square).

Unit V

Concept of Probability : Probability Mass function, Probability density function. Discrete Distribution: Binomial, Poisson's, Continuous Distribution: Normal Distribution, Exponential Distribution ,Gamma Distribution ,Beta Distribution ,Testing of Hypothesis |:Students t-test, Fisher's z-test, Chi-Square Method

Reference:

- (i) Numerical Methods using Matlab by J.H.Mathews and K.D.Fink, P.H.I.
- (ii) Numerical Methods for Scientific and Engg. Computation by MKJain, Iyengar and RK Jain, New Age International Publication
- (iii) Mathematical Methods by KV Suryanarayan Rao, SCITECH Publuication (
- iv) Numerical Methods using Matlab by Yang, Wiley India
- (v) Pobability and Statistics by Ravichandran ,Wiley India
- (vi) Mathematical Statistics by George R., Springer



SARVEPALLI RADHAKRISHNAN UNIVERSITY, **BHOPAL**

CS- 302 - COMPUTER SYSTEM ORGANIZATION

Unit I

Computer Basics and CPU: Von Newman model, various subsystems, CPU, Memory, I/O, System Bus, CPU and Memory registers, Program Counter, Accumulator, Instruction register, Micro operations, Register Transfer Language, Instruction Fetch, decode and execution, data movement and manipulation, Instruction formats and addressing modes of basic computer. 8085 microprocessor organization

Unit-II

Control Unit Organization: Hardwired control unit, Micro and nano programmed control unit, Control Memory, Address Sequencing, Micro Instruction formats, Micro program sequencer, Microprogramming, Arithmetic and Logic Unit: Arithmetic Processor, Addition, subtraction, multiplication and division, Floating point and decimal arithmetic and arithmetic units, design of arithmetic unit.

Unit-III

Input Output Organization: Modes of data transfer - program controlled, interrupt driven and direct memory access, Interrupt structures, I/O Interface, Asynchronous data transfer, I/O processor, 8085 I/O structure, 8085 instruction set and basic programming. Data transfer - Serial / parallel, synchronous/asynchronous, simplex/half duplex and full duplex.

Unit-IV

Memory organization: Memory Maps, Memory Hierarchy, Cache Memory - Organization and mappings. Associative memory, Virtual memory, Memory Management Hardware.

Unit V

Multiprocessors: Pipeline and Vector processing, Instruction and arithmetic pipelines, Vector and array processors, Interconnection structure and inter-processor communication.

References:

1. Morris Mano: Computer System Architecture, PHI.
2. Tanenbaum: Structured Computer Organization, Pearson Education
3. J P Hayes, Computer Architecture and Organisations, Mc- Graw Hills, New Delhi
4. Gaonkar: Microprocessor Architecture, Programming, Applications with 8085; Penram Int.
5. William Stallings: Computer Organization and Architecture, PHI
6. ISRD group; Computer orgOrganization; TMH
7. Carter; Computer Architecture (Schaum); TMH
8. Carl Hamacher: Computer Organization, TMH



SARVEPALLI RADHAKRISHNAN UNIVERSITY, **BHOPAL**

CS- 303 Digital Circuit & System

Unit I

Number systems & codes, Binary arithmetic , Boolean algebra and switching function. Minimization of switching function, Concept of prime implicant , Karnaugh map method, Quine & McCluskey's method, Cases with don't care terms, Multiple output switching function.

Unit II

Introduction to logic gates, Universal gate, Half adder, Half subtractor, Full adder, Full subtractor circuits, Series & parallel addition , BCD adders, Look-ahead carry generator.

Unit III

Linear wave shaping circuits, Bistable, Monostable & Astable multivibrator, Schmitt trigger circuits & Schmitt-Nand gates. Logic families : RTL, DTL, All types of TTL circuits , ECL, I²L , PMOS, NMOS & CMOS logic, Gated flip-flops and gated multivibrator , Interfacing between TTL to MOS.

Unit IV

Decoders, Encoders, Multiplexers, Demultiplexers, Introduction to various semiconductor memories & designing with ROM and PLA. Introduction to Shift Registers, Counters, Synchronous & asynchronous counters, Designing of Combinational circuits like code converters.

Unit V

Introduction of Analog to Digital & Digital to Analog converters, sample & hold circuits and V-F converters.

References:

1. M. Mano; "Digital Logic & Computer Design"; PHI.
2. Malvino & Leach; "Digital Principles & Applications"; TMH
3. W.H. Gothman; "Digital Electronics"; PHI.
4. Millman & Taub; "Pulse, Digital & Switching Waveforms"; TMH
5. Jain RP; Modern digital Electronics; TMH
6. R.J. Tocci, "Digital Systems Principles & Applications".

List of experiment (Expandable)

1. To study and test of operation of all logic gates for various IC's (IC#7400,IC#7403,IC#7408,IC#74332,IC#7486).
2. Verification of Demorgan's theorem.
3. To construct of half adder and full adder
4. To construct of half subtractor and full subtractor circuits
5. Verification of versatility of NAND gate.
6. Verification of versatility of NOR gate.
7. Designing and verification of property of full adder.
8. Design a BCD to excess-3 code converter.
9. Design a Multiplexer/ Demultiplexer.



SARVEPALLI RADHAKRISHNAN UNIVERSITY, **BHOPAL**

CS- 304 Java Technology

UNIT-I

Basic Java Features - C++ Vs JAVA, JAVA virtual machine, Constant & Variables, Data Types, Class, Methods, Objects, Strings and Arrays, Type Casting, Operators, Precedence relations, Control Statements, Exception Handling, File and Streams, Visibility, Constructors, Operator and Methods Overloading, Static Members, Inheritance: Polymorphism, Abstract methods and Classes

UNIT-II

Java Collective Frame Work - Data Structures: Introduction, Type-Wrapper Classes for Primitive Types, Dynamic Memory Allocation, Linked List, Stack, Queues, Trees,

Generics: Introduction, Overloading Generic Methods, Generic Classes, Collections: Interface Collection and Class Collections, Lists, Array List and Iterator, Linked List, Vector.

Collections Algorithms: Algorithm sorts, Algorithm shuffle, Algorithms reverse, fill, copy, max and min Algorithm binary Search, Algorithms add All, Stack Class of Package java. Util, Class Priority Queue and Interface Queue, Maps, Properties Class, Un-modifiable Collections.

UNIT-III

Advance Java Features - Multithreading: Thread States, Priorities and Thread Scheduling, Life Cycle of a Thread, Thread Synchronization, Creating and Executing Threads, Multithreading with GUI, Monitors and Monitor Locks. Networking: Manipulating URLs, Reading a file on a Web Server, Socket programming, Security and the Network, RMI, Networking, Accessing Databases with JDBC: Relational Database, SQL, MySQL, Oracle

UNIT-IV

Advance Java Technologies - Servlets: Overview and Architecture, Setting Up the Apache Tomcat Server, Handling HTTP get Requests, Deploying a web Application, Multitier Applications, Using JDBC from a Servlet, Java Server Pages (JSP): Overview, First JSP Example, Implicit Objects, Scripting, Standard Actions, Directives, Multimedia: Applets and Application: Loading, Displaying and Scaling Images, Animating a Series of Images, Loading and playing Audio clips

UNIT-V

Advance Web/Internet Programming (Overview): J2ME, J2EE, EJB, XML.

References:

1. Deitel & Deitel, "JAVA, How to Program"; PHI, Pearson.
2. E. Balaguruswamy, "Programming In Java"; TMH Publications
3. The Complete Reference: Herbert Schildt, TMH
4. Peter Norton, "Peter Norton Guide To Java Programming", Techmedia.
5. Merlin Hughes, et al; Java Network Programming , Manning Publications/Prentice Hall
6. Cay Horstmann, Big JAVA, Wiely India.

List of Program to be perform (Expandable)

1. Installation of J2SDK
2. Write a program to show Scope of Variables
3. Write a program to show Concept of CLASS in JAVA
4. Write a program to show Type Casting in JAVA
5. Write a program to show How Exception Handling is in JAVA
6. Write a Program to show Inheritance

7. Write a program to show Polymorphism
8. Write a program to show Access Specifiers (Public, Private, Protected) in JAVA
9. Write a program to show use and Advantages of CONSTRUCTOR
10. Write a program to show Interfacing between two classes
11. Write a program to Add a Class to a Package
12. Write a program to show Life Cycle of a Thread
13. Write a program to demonstrate AWT.
14. Write a program to Hide a Class
15. Write a Program to show Data Base Connectivity Using JAVA
16. Write a Program to show "HELLO JAVA " in Explorer using Applet
17. Write a Program to show Connectivity using JDBC
18. Write a program to demonstrate multithreading using Java.
19. Write a program to demonstrate applet life cycle.
20. Write a program to demonstrate concept of servlet.



SARVEPALLI RADHAKRISHNAN UNIVERSITY, **BHOPAL**

CS- 305 Data Structures

Unit I

Introduction: Basic Terminology, Data types and its classification, Algorithm complexity notations like big Oh Array Definition, Representation and Analysis of Arrays, Single and Multidimensional Arrays, Address calculation, Array as Parameters, Ordered List and operations, Sparse Matrices, Storage pools, Garbage collection. Recursion-definition and processes, simulating recursion, Backtracking, Recursive algorithms, Tail recursion, Removal of recursion. Tower of Hanoi Problem.

UNIT II

Stack, Array Implementation of stack, Linked Representation of Stack, Application of stack: Conversion of Infix to Prefix and Postfix Expressions and Expression evaluation, Queue, Array and linked implementation of queues, Circular queues, D-queues and Priority Queues. Linked list, Implementation of Singly Linked List, Two-way Header List, Doubly linked list, Linked List in Array. Generalized linked list, Application: Garbage collection and compaction, Polynomial Arithmetic.

UNIT III

Trees: Basic terminology, Binary Trees, , algebraic Expressions, Complete Binary Tree, Extended Binary Trees, Array and Linked Representation of Binary trees, Traversing Binary trees, Threaded Binary trees, Binary Search Tree (BST), AVL Trees, B-trees. Application: Algebraic Expression, Huffman coding Algorithm.

UNIT IV

Internal and External sorting ,Insertion Sort, Bubble Sort, selection sort Quick Sort, Merge Sort, Heap Sort, Radix sort, Searching & Hashing: Sequential search, binary search, Hash Table, Hash Functions, Collision Resolution Strategies, Hash Table Implementation. Symbol Table, Static tree table, Dynamic Tree table.

Unit V

Graphs: Introduction, Sequential Representations of Graphs, Adjacency Matrices, Traversal, Connected Component and Spanning Trees, Minimum Cost Spanning Trees.

Reference:

1. R. Kruse et al, "Data Structures and Program Design in C", Pearson Education Asia, Delhi-2002
2. ISRD Group; Data structures using C; TMH
3. Lipschutz; Data structure (Schaum); TMH
4. Horowitz and Sahani, "Fundamentals of data Structures", Galgotia Publication Pvt. Ltd., N Delhi. A.
5. M. Tenenbaum, "Data Structures using C & C++", Prentice-Hall of India Pvt. Ltd., New Delhi.
6. Data Structures Trembley and Sorenson, TMH Publications
7. Pai; Data structure and algorithm; TMH
7. Introduction to Algorithm- Corman, AWL

List of Experiments (expandable):

Programs in C relating to different theory units.



SARVEPALLI RADHAKRISHNAN UNIVERSITY, **BHOPAL**

CS 306 HARDWARE INSTTALATION AND MAINTENANCE

PURPOSE

This course is designed to enable the students to get a detailed knowledge of all the hardware components that make up a computer and to understand the different interfaces required for connecting these hardware devices.

INSTRUCTIONAL OBJECTIVES

1. Understand the components on the motherboard
2. Perform system administration tasks
3. Understand different storage media
4. Understand system related problems and methods of troubleshooting

LIST OF EXPERIMENTS

1. Study and Identification of standard desktop personal computer
2. Understanding of Motherboard and its interfacing components
3. Install and configure computer drivers and system components.
4. Disk formatting, partitioning and Disk operating system commands
5. Install, upgrade and configure Windows operating systems.
6. Remote desktop connections and file sharing.
7. Identify, Install and manage network connections Configuring IP address and Domain name system
8. Install, upgrade and configure Linux operating systems.
9. Installation Antivirus and configure the antivirus.
10. Installation of printer and scanner software.
11. Disassembly and Reassembly of hardware.
12. Trouble shooting and Managing Systems

REFERENCES

1. Craig Zacker& John Rourke, "*The complete reference : PC hardware*", Tata McGraw- Hill, New Delhi, 2001.
2. Mike Meyers, "*Introduction to PC Hardware and Troubleshooting*", Tata McGraw-Hill, New Delhi, 2003.
3. Govindarajulu B., "*IBM PC and Clones hardware trouble shooting and maintenance*", Tata McGraw-Hill, New Delhi, 2002.



SARVEPALLI RADHAKRISHNAN **UNIVERSITY, BHOPAL**

CS-307 Seminar / Group Discussion(Internal Assessment)

Objective of GD and seminar is to improve the MASS COMMUNICATION and CONVINCING/ understanding skills of students and it is to give student an opportunity to exercise their rights to express themselves.

Evaluation will be done by assigned faculty based on group discussion and power point presentation.



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CS-308 Critical Thinking (Internal Assessment)

- What is critical thinking?
- Claims, issues, and arguments
- Argument structure
- Ways to be imprecise
- Evaluating claims
- Arguing fairly
- Consistency and contradiction
- Deductive logic
- Explanations
- Inductive Arguments
- Scientific reasoning